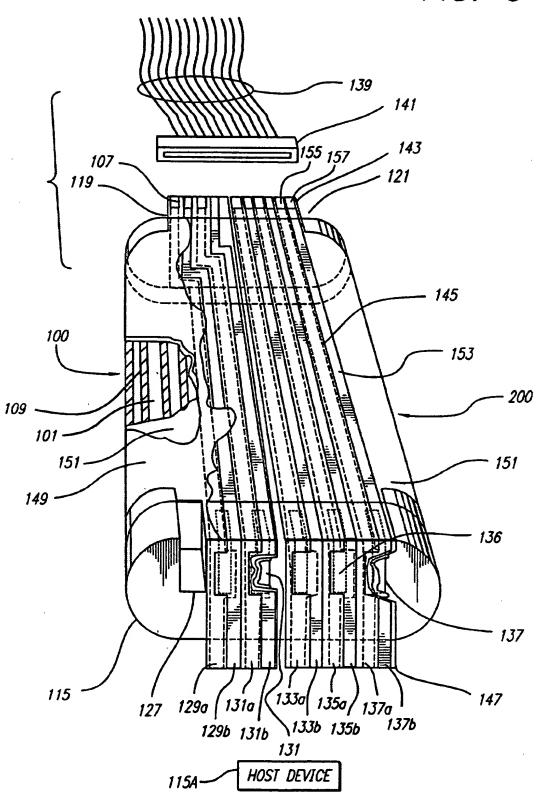
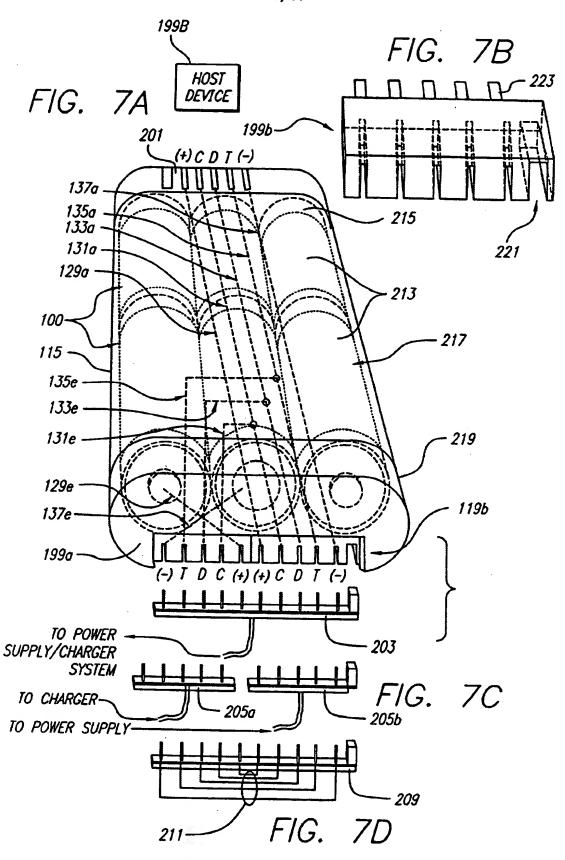
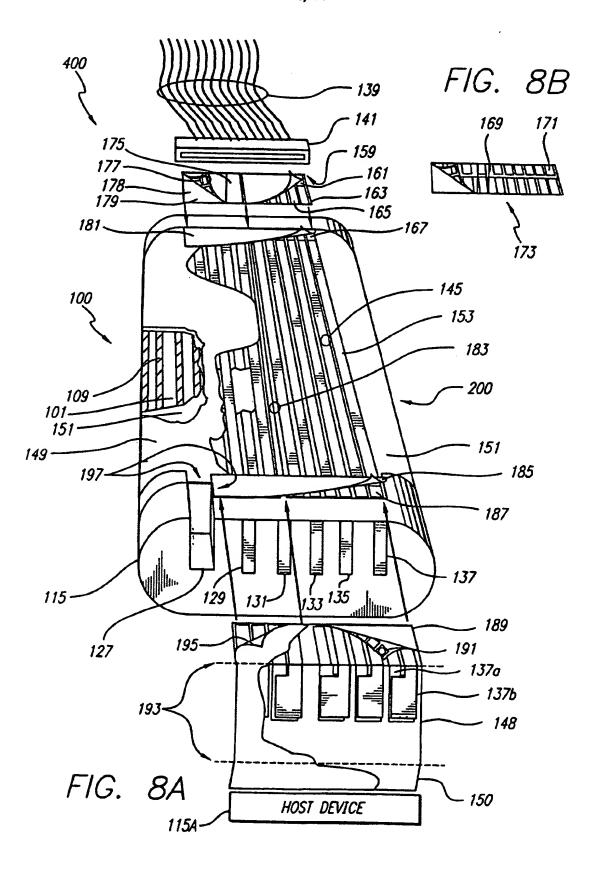
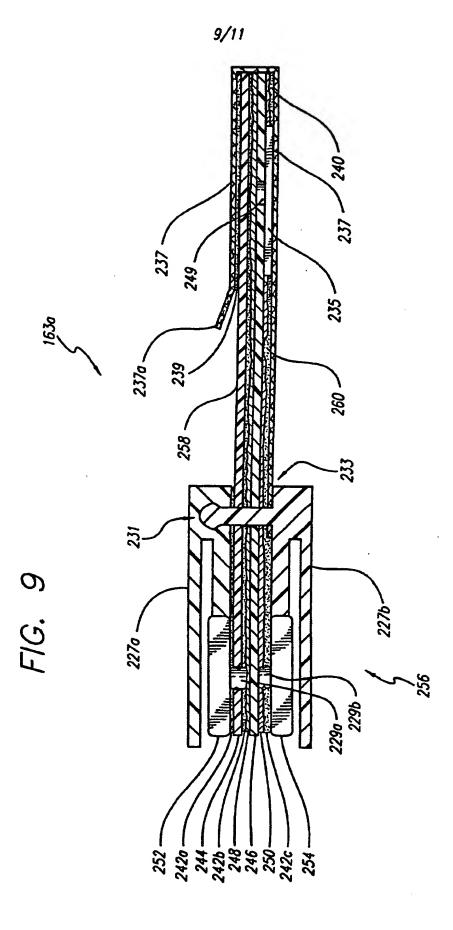


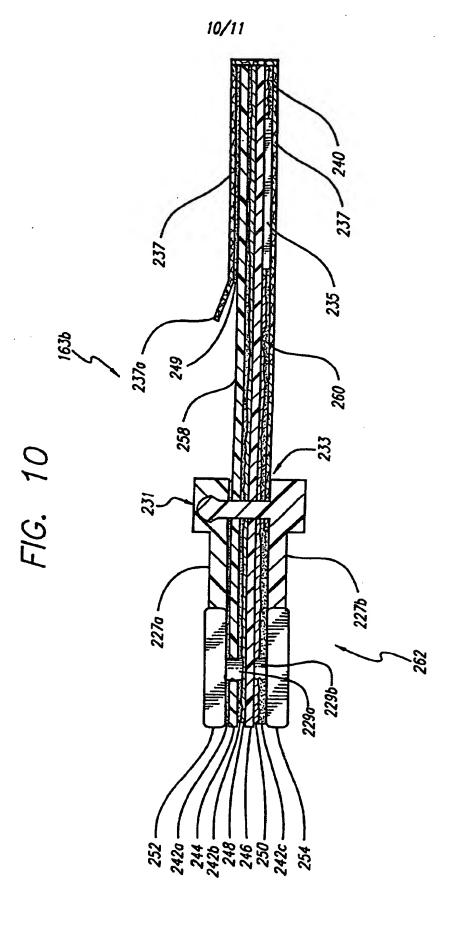
FIG. 6

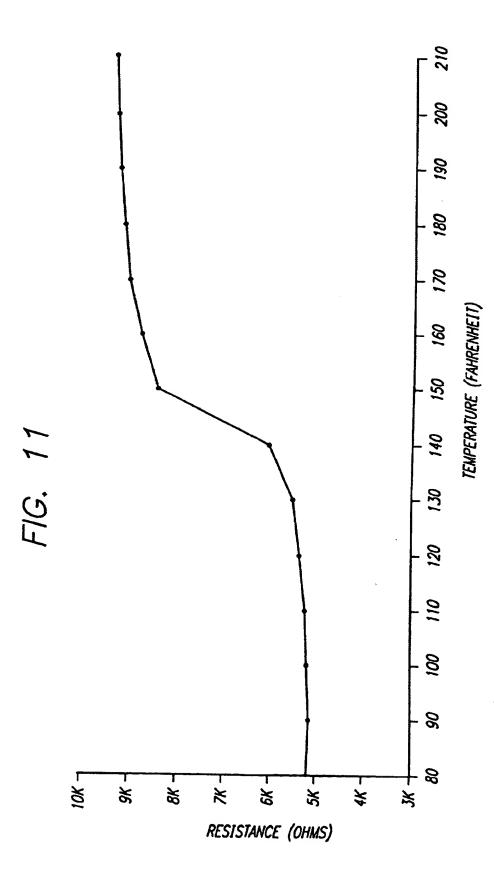


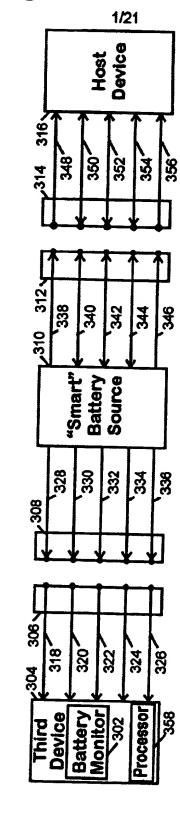












2*1*21 Device External Regulator Data VO Hos USB 3860 399 3708 383 381 393 375 361 r376 Source Battery Add-On Thermistor 391 373 369 398 365 363 367 7374 ,362 372_V 384 386 ब्रुह 388 392 390 386 397 388 Converter Voltage Regulator Third Device A/D Converter Processor USB Comm Circuit **Q** 366 388

FIG. 13

Demodulator Modulator/ Host Device 63 457 Thermistor Battery Source 413 Add-On FIG. 14A 455 449 447 451 405 407 語 433 431 437 439 433 423 -421 **4**63 Charger Demodulator 465 Battery Regulator A/D Converter Power Source Modulator/ Third Device Processor 429 419 427

477 Host Device USB M/D /463B Data 10 Port Regulator 405B A 417A **489** 463A 164 477A 7405A Modulator/ Demodulator 463 USB \$ Thermistor **Battery** Source Adapter Add-On FIG. 14B ₩, **4** 45 44 154 455 405 407 431 433 437 439 423 <u>\$</u> Demodulator Battery Charger A/D Converter 465 Regulator Modulator/ Source Third Device Power Processor 429 427 402

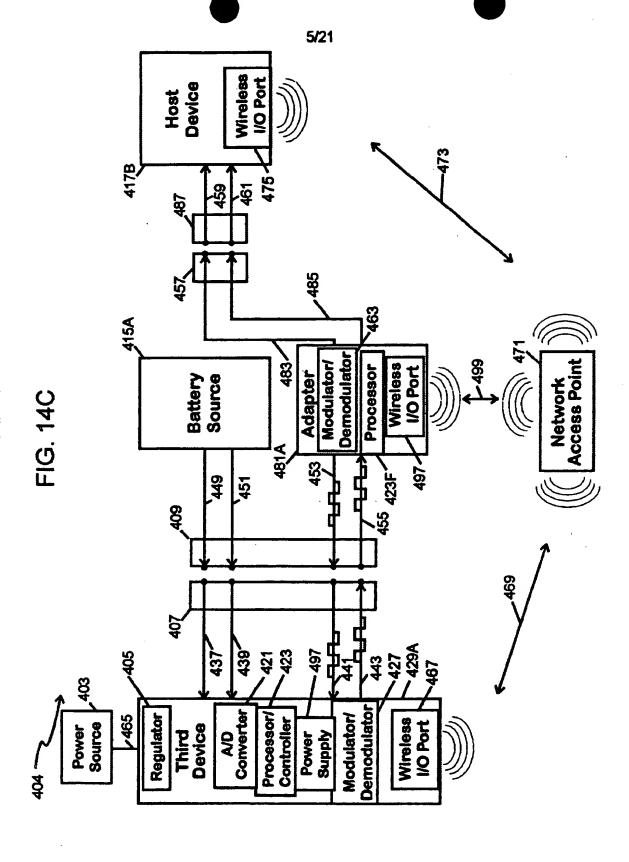
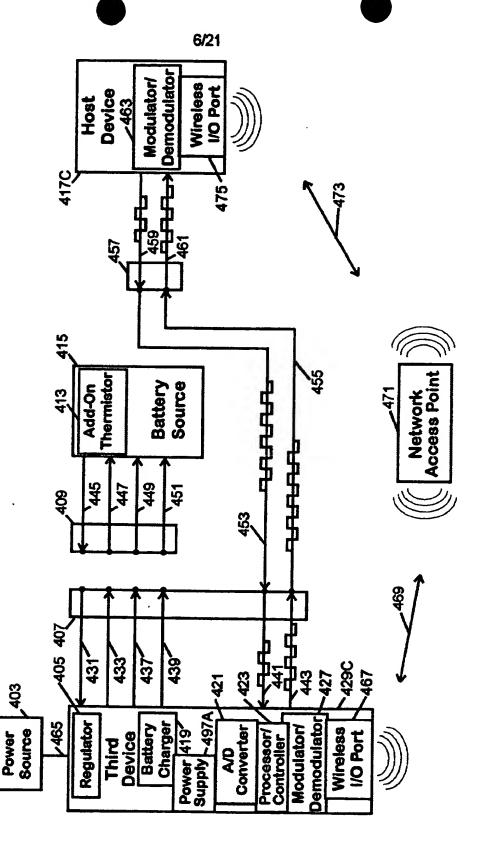


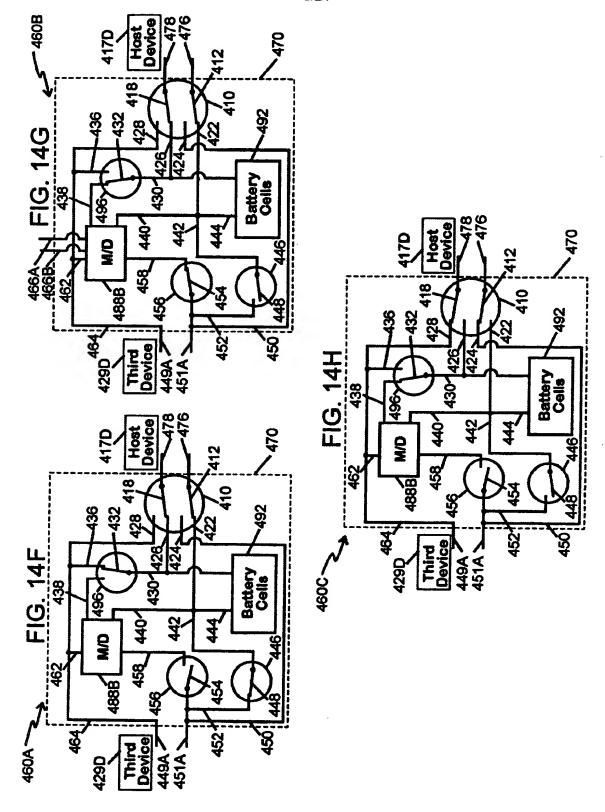
FIG. 14D



Processor Wireless NO Port Device Host **-483** Memory 475 कृ 478 "Smart" Battery Circuit Source Battery Demodulator 415B **Modulator/** ##**4** Network Access Point Processor/ Memory Controller Switch § 423B 474B 1024 468B, 492 .488A 439A ~ 405 437A. 470A 497B -429D 468A 474A 423A 467 Demodulator Switch A/D Converter Power Source \$ Modulator Wireless Third Device Regulator Processor/ 10 Port Memory Controller Supply Power

FIG. 14E





Processor Wireless NO Port Memory Device Host 475~ 461 415C 476 "Device Emulator" Controller Memory "Smart" Processor Battery Switch Circui Network Access Point Controller Demodulator Processor/ Controller Wireless I/O Port Memory Power Converter Adapter Modulator Switch 423D 488C, (D) 470C, (D) (488C -7 1497C -423C 429E 474C 465 467 Demodulator **Modulator/** 465 Processor/ Controller Wireless Source Regulator Third Device Power Supply 10 Port Power Switch Memory

FIG. 141

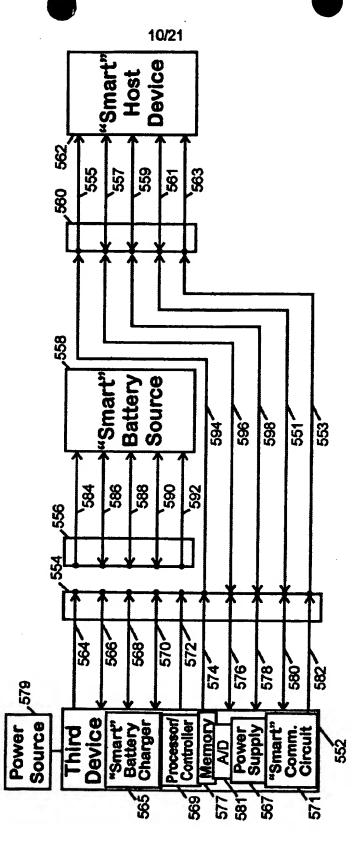
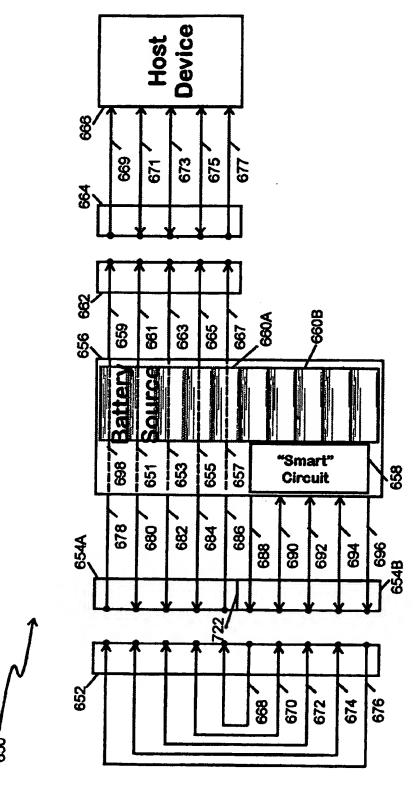
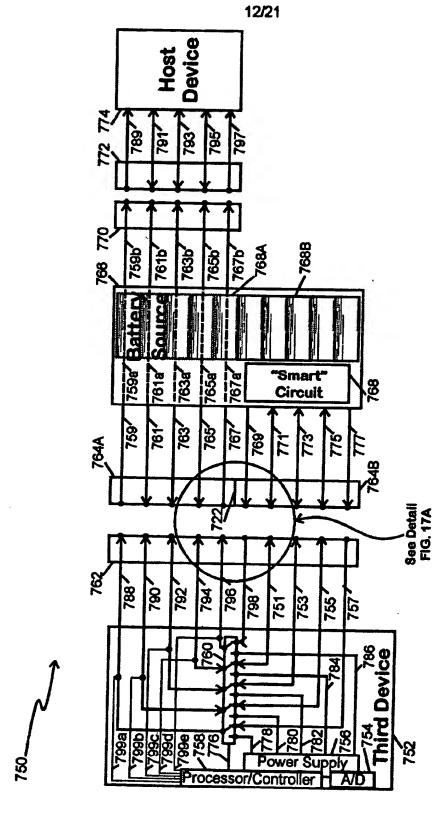
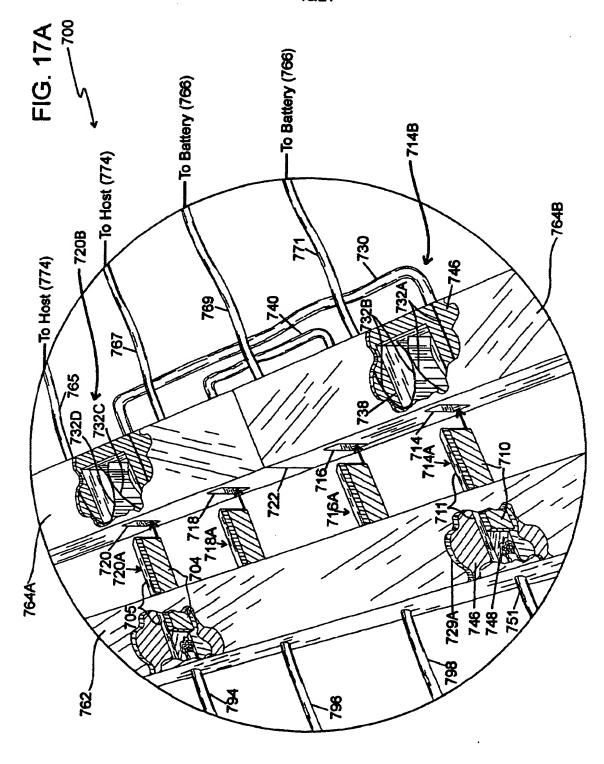


FIG. 16







CONTROL OF TOTAL

FIG. 17B

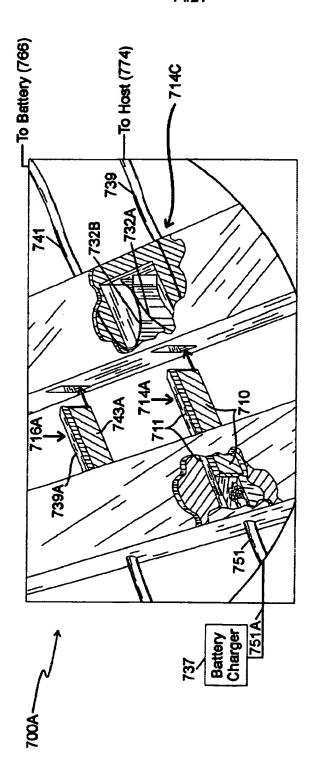


FIG. 17C

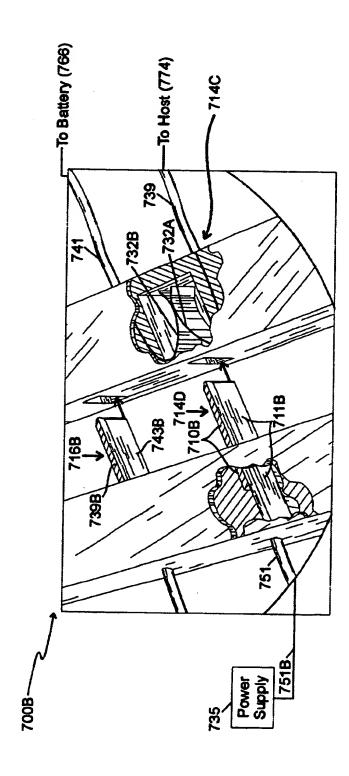


FIG. 18A

